COMBINED DECLARATION/POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

an original, first and	joint inventor (if p	st and sole inventor (if only on lural names are listed below) of the invention entitled	
METHOD	FOR FABRICAT	ING SEMICONDUCTOR DE	VICE e specification of which
			e specification of which
(check one)	X is attache	d hereto.	•
	was filed o	onon Serial No	as
_		ewed and understand the contented by any amendment refer	
	· .	close information which is mate 37, Code of Federal Regulation	
any foreign applica	tion(s) for patent foreign applicatio	y benefits under Title 35, Unit or inventor's certificate list on for patent or inventor's certi- priority is claimed:	ed below and have also
any foreign applica identified below any	tion(s) for patent foreign applicatio dication on which p	or inventor's certificate listen for patent or inventor's certi	ed below and have also
any foreign application identified below any before that of the appropriate Prior Foreign Application	tion(s) for patent foreign application lication on which pation(s)	or inventor's certificate listen for patent or inventor's certionity is claimed:	ed below and have also ficate having a filing date Priority Claimed
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any foreign applicated identified below any before that of the appropriate Prior Foreign Application 2002-283684 (Number)	tion(s) for patent foreign application on which pation(s) JAPAN (Country)	or inventor's certificate lister or for patent or inventor's certification for patent or inventor's certificate lister or i	ed below and have also ficate having a filing date Priority Claimed X Yes No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Appln. Serial No.)	(Filing Date)	(Status-patented,	pending, abandoned)
(Appln. Serial No.)	(Filing Date)	(Status-patented,	pending, abandoned)
prosecute the patent as and Trademark Office Jr. (Reg. No. 28,149) 28,562); Paul Devinsk E. Fogarty (Reg. No.	at as my attorneys, with pplication identified above connected therewith: Rate, Kenneth L. Cage (Reg. y (Reg. No. 28,553); Edw. 36,139); Brian E. Ferguse dem F. Gadiano (Reg. No.	e and to transact all but the phael V. Lupo (Reg. Ng. No. 26,151); Stanis and E. Kubasiewicz (Ron (Reg. No. 36,801);	o. 28,363); Jack Q. Lever laus Aksman (Reg. No. leg. No. 30,020), Michael
Please address all corre	espondence and telephone	calls to:	
	Jack Q. Lever, Jr. McDERMOTT, WILI 600 Thirteenth Street, Washington, D.C. 20 (202) 756-8000	N.W.	
instructions from Trademark Office reg attorney and the under	I hereby authorizes the U Maeda Patent Office arding this application we signed. In the event of attorneys named herein we	_ as to any action to be ithout direct communa change in the person	be taken in the Patent and ication between the U.S. s from whom instructions
all statements made of statements were made punishable by fine or i	are that all statements made on information and belief with the knowledge that mprisonment, or both, un- llful false statements may	are believed to be trowillful false statements der Section 1001 of Tit	ue; and further that these and the like so made are tle 18 of the United States
Full name of sole or fin	st inventor	Kazutoshi ONO	ZAWA
Inventor's signature Residence*	K - Omyaw Osaka, Japan	Date Citizenshi	May 22, 2003 p Japan
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^{*} City and State, or City and Country for foreign inventors

PATENT

Docket No.: 60188-541

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kazutoshi ONOZAWA

Serial No.:

Group Art Unit:

Filed: June 24, 2003

Examiner:

For:

METHOD FOR FABRICATING SEMICONDUCTOR DEVICE

ASSOCIATE POWER OF ATTORNEY

Mail Stop Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The undersigned Principal Attorney of record hereby appoints the following Attorneys as his Associates with regard to the above-identified application: Steven W. Allis, Reg. No. 50,532; Stephen A. Becker, Reg. No. 26,527; John G. Bisbikis, Reg. No. 37,095; Daniel Bucca, Reg. No. 42,368; Kenneth L. Cage, Reg. No. 26,151; Jennifer Chen, Reg. No. 42,404; Bernard P. Codd, Reg. No. 46,429; Lawrence T. Cullen, Reg. No. 44,489; Paul Devinsky, Reg. No. 28,553; Margaret M. Duncan, Reg. No. 30,879; Shamita De. Etienne-Cummings, Reg. No. 46,072; Ramyar M. Farid, Reg. No. 46,692; Brian E. Ferguson, Reg. No. 36,801; Michael E. Fogarty, Reg. No. 36,139; John R. Fuisz, Reg. No. 37,327; Willem F. Gadiano, Reg. No. 37,136; Keith E. George, Reg. No. 34,111; John A. Hankins, Reg. No. 32,029; Eric J. Kraus, Reg. No. 36,190; Catherine Krupka, Reg. No. 46,227; Jack Q. Lever, Reg. No. 28,149; Raphael V. Lupo, Reg. No. 28,363; Burman Y. Mathis III, Reg. No. 44,907; Michael A. Messina, Reg. No. 33,424; Dawn L. Palmer, Reg. No. 41,238; Joseph H. Paquin, Jr., Reg. No. 31,647; Scott D. Paul, Reg. No. 42,984; William D. Pegg, Reg. No. 42,988; Robert L. Price, Reg. No. 22,685; Gene Z. Rubinson, Reg. No. 33,351; Brian K. Seidleck, Reg. No. 51,321; Joy Ann G. Serauskas, Reg. No. 27,952; Jiri F. Smetana, Reg. No. 52,456; Spenard, Reg. No. 37,449; Arthur J. Steiner, Reg. No. 26,106; Wesley Strickland, Reg. No. 44,363; Michael D. Switzer, Reg. No. 39,552; David M. Tennant, Reg. No. 48,362; Judith L. Toffenetti, Reg. No. 39,048; Daniel S. Trainor, Reg. No. 43,959; Shival P. Virmani, Reg. No. 45,032; Kelli N. Watson, Reg. No. 47,170; Cameron K.

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Please continue to address all communications to the undersigned.

June 24, 2003

By_

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